

ABSTRACT

A power amplifier includes a transconductance stage, a cascode stage, and a connector. The transconductance stage is operable to receive an input voltage signal and to produce an output current signal. The cascode stage communicatively couples to the
5 transconductance stage and is operable to receive the output current signal and to produce an output voltage signal based thereupon. The cascode stage includes a Metal Oxide Silicon (MOS) transistor and a corresponding parasitic bipolar transistor formed in parallel therewith in a semi conductive substrate. The MOS transistor has a drain, a gate, and a source. The corresponding parasitic bipolar junction transistor has a collector
10 corresponding to the drain, an emitter corresponding to the source, and a base corresponding to the semi conductive substrate. The connector couples the base of the corresponding parasitic bipolar junction transistor to the source of the MOS transistor.